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Total # of Pages 13 (including this page)

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Examiner S. Rao Group Art Unit 2814 U.S. Patent and Trademark Office	703-306-5945	703-746-3926					

From: Aaron C. Chatterjee (Reg. 41,398)

Sender's Direct Dial: 202-945-6117

Date: August 4, 2003

Appln. Serial No. 09/440,928 Filed: November 16, 1999 Nobutoshi AOKI et al. Atty Dkt No. 040301/0578

User ID No: 0201

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TECHNOLOGY CENTER 2800

I hereby certify that the following:

Supplemental Amendment and Reply Under 37 CFR § 1.111 Amendment Transmittal

was transmitted to the U.S. Patent and Trademark Office Group 2800 by facsimile, addressed to:

Examiner S. Rao Group Art Unit 2814

U.S. Patent and Trademark Office

Arlington, Virginia/

on August 4, 2003 in the above-identified application.

Karyon Owen (secretary to

Aaron C. Chatterjee, Reg. No. 41,398)

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Attorney Docket 040301-0578

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Nobutoshi AOKI et al.

Title:

SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING INSULATED

GATE FIELD EFFECT TRANSISTOR AND METHOD OF

MANUFACTURING THE SAME

Appl. No.:

09/440,928

Filing Date:

11/16/1999

Examiner:

S. Rao

Art Unit:

2814

<u>AMENDMENT</u> TRANSMITTAL

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MAIL STOP NON-FEE AMENDMENT Commissioner for Patents PO Box 1450 Alexandria, Virginia 22313-1450

AUG - 4 2003

TECHNOLOGY CENTER 2800

Sir:

Transmitted herewith is an amendment in the above-identified application.

- Small Entity status under 37 C.F.R. § 1.9 and § 1.27 has been established by a [] Small Entity statement previously submitted.
- [] Small Entity statement is enclosed.
- [X] The fee required for additional claims is calculated below:

	Claims as Amended		Previously Paid For		Extra Claims Presen	S	Rate	•	Additional Claims Fee
Total Claims:	28	_	31	=	0	×	\$18.00		\$0.00
Independents:	2	-	10	=	0	x	\$84.00	==	\$0.00
First presentation of any Multiple Dependent Claims:			+	\$280.00	=	\$0.00			
						CLAIMS	FEE TOTAL:	=	\$0.00

[] Applicant hereby petitions for an extension of time under 37 C.F.R. §1.136(a) for the total number of months checked below:







Application No. 09/440,928

Attorney Docket 040301-0578

	Extension for response filed within the first month:	\$110.00	\$0.00
()	Extension for response filed within the second month:	\$41Q.00	\$0.00
[]	Extension for response filed within the third month:	\$930.00	\$0.00
[]	Extension for response filed within the fourth month:	\$1,450.00	\$0.00
[]	Extension for response filed within the fifth month:	\$1,970.00	\$0.00
	EXTENSION	N FEE TOTAL:	\$0.00
[]	Statutory Disclaimer Fee under 37 C.F.R. 1.20(d):	\$110.00	\$0.00
	CLAIMS, EXTENSION AND DISCLAIME	R FEE TOTAL:	\$0.00
[]	Small Entity Fees Apply (subtract	½ of above):	\$0.00
		TOTAL FEE:	\$0.00

- Please charge Deposit Account No. 19-0741 in the amount of \$0.00. A [] duplicate copy of this transmittal is enclosed.
- [X] A check in the amount of \$0.00 is enclosed.
- The Commissioner is hereby authorized to charge any additional fees which may [X] be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741. If any extensions of time are needed for timely acceptance of papers submitted herewith, applicant hereby petitions for such extension under 37 C.F.R. §1.136 and authorizes payment of any such extensions fees to Deposit Account No. 19-0741.

Please direct all correspondence to the undersigned attorney or agent at the address indicated below.

Respectfully submitted,

Date August 4, 2003

FOLEY & LARDNER

Customer Number: 22428

22428

PATENT TRADEMARK OFFICE

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By_ Aaron C. Challegee

Richard L. Schwaab Attorney for Applicant Registration No. 25,479

Aaron C. Chatterjee Registration No. 41,398

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-2-

TECHNOLOGY CENTER 2800







Attorney Docket 040301-0

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Nobutoshi AOKI et al.

Title:

SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING INSULATED

GATE FIELD EFFECT TRANSISTOR AND METHOD OF MANUFACTURING

THE SAME

Appl. No.:

09/440,928

Filing Date: 11/16/1999

Examiner:

S. Rao

Art Unit:

2814

SUPPLEMENTAL AMENDMENT AND REPLY UNDER 37 C.F.R. § 1.111

Mail Stop NON-FEE AMENDMENT

Commissioner for Patents PO Box 1450

Alexandria, Virginia 22313-1450

Sir:

This communication is in further reply to the Non-Final Office Action dated February 25, 2003 and supplemental to the Amendment filed May 27, 2003, concerning the above-referenced patent application.

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this document.

Remarks/Arguments begin on page 2 of this document.

Please amend the application as follows:

